

**INDIAN INSTITUTE OF  
INFORMATION  
TECHNOLOGY KOTTAYAM**

**INTELLIGENT INTEGRATED  
CIRCUITS & SYSTEMS (I<sup>2</sup>CS)  
RESEARCH LAB**

[www.i2cs.iitkottayam.ac.in](http://www.i2cs.iitkottayam.ac.in)

**Hands-On Workshop on  
Digital Design Flow using SYNOPSIS  
EDA Tool**



**MARCH 7, 2025**

**11:00AM TO 12:30PM**

**TOPICS AT A GLANCE**

- Introduction to Digital IC Design Flow
- Introduction to Industry standard EDA Tool: SYNOPSIS
- Simulation and synthesis using VCS, VERDI and Design Compiler

**Participants should bring Laptop**

**ELIGIBILITY**

Anyone with passion towards VLSI Design. Knowledge in Verilog Hardware Description Language (HDL) is desirable.

Registration is mandatory.

**Deadline:** 6th March 2025



Indian Institute of  
Information Technology  
Kottayam



Intelligent Integrated  
Circuits and Systems  
Research Group



IEEE CIRCUITS AND SYSTEMS SOCIETY

**Venue: I<sup>2</sup>CS Research Lab, IITK**

**SPEAKER**

**DR. KALA S**

**ASSISTANT PROFESSOR,  
DEPARTMENT OF ECE**

**About I<sup>2</sup>CS Research Lab**

We are engaged in various funded and collaborative projects in the area of VLSI and Embedded Systems. Research lab is equipped with state-of-the-art EDA tools for IC design under **Chips to Startup (C2S) programme of MeitY**, Govt of India.

Facilities in research lab include EDA Tools from Synopsys, Cadence, Siemens, and AMD Xilinx FPGA boards, MicroChip SoC boards, Nvidia Jetson, Robotics kits, Raspberry Pi, Arduino boards and sensors, Matlab etc.

[More information](#)

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**REGISTRATION**

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