



Short Term Training Program (STTP) on Rapid Prototyping using FPGA

June 16-21, 2025

Indian Institute of Information
Technology Kottayam





I²CS Research Lab

Department of ECE

Valavoor P O, Kottayam-686635, Kerala

 i2cs.iiitkottayam.ac.in

What You'll Learn:




-  **Hardware modeling using Verilog HDL**
Learn the basics of Verilog HDL
-  **High Level Synthesis**
Introduction to HLS with sample programs
-  **Hands-On using AMD Xilinx FPGA boards**
Familiarize AMD Xilinx tool flow for implementation on hardware
-  **Mini projects on FPGA boards**
Get practical experience by doing simple projects by your own!

Certificate will be provided after successful completion of STTP

Who can attend?

UG/PG/PhD/Academicians/
Working Professionals

Event Details

-  **Deadline: 2nd June, 2025**
-  **9.30 AM - 4.30 PM**
-  **I²CS Research Lab
(Hybrid Mode)**

Registration Fee

Rs. 1500/- for UG/PG
Rs. 2000/- for others

Accommodation

Limited number, on payment basis

Reserve Your Spot Now!

Check WEBSITE for details

Registration Link:

<https://forms.gle/87RE9NuPvME7s6Ej9>

ORGANIZERS

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Payment Details

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